

Georgia Tech Symposium

Centers of Competence

Doreen Anding, IBM
July 10-11, 2008

CoC Goals

- Sony Toshiba IBM (STI) ecosystem team originated in 2006
- Chief goals were to establish 3 Centers of Competence (CoC) in three different geos: Asia, Europe and Americas
- These centers have developed:
 - research programs
 - remote access programs
 - public symposiums/fairs
 - information dissemination (wikis)
 - through presentations, and publications have evangelized the Cell/B.E. technology around the world.

3rd STI CoC: Trinity College Dublin



- **First European CoC – located in Dublin, Ireland**
- **Michael Manzke, PhD: Lecturer in Computer Science; Co-Director of the TCD CoC**
- **Research Group: Graphics, Vision and Visualisation Group ([GV2](#))**
- **Research Areas: *Computer Graphics, Computer Architecture and Graphics Hardware***
 - **Compute Clusters and Real Time Compute Clusters**
 - **Performance Analysis**
 - **Intrusive and non-Intrusive Trace Data Acquisition and Analysis**
 - **Kalman Filtering**
 - **High Speed Interconnects**
 - **Scalable Coherent Interface (SCI)**

2nd STI CoC: Japan - Kitakyushu/FAIS

- **Foundation for the Advancement of Industry and Science Technology (FAIS) was the first Asian CoC.**
- **Unique in that it has a consortium of universities (Waseda University, University of Kitakyushu, and Kyushu Institute of Technology)**
- **Entering the second year as CoC.**
- **Yasuhiko Tanaka is the Executive Director, Application Engineering Department, Semiconductor Engineering Center, Kitakyushu Foundation for the Advancement of Industry, Science and Technology (FAIS)**
- **Tanaka-san is promoting industry-academia collaboration and coordination between the universities' research and meeting the business needs in Kitakyushu area.**
- **Tanaka-san obtained a master's degree in nuclear engineering from Kyushu University.**



1st STI CoC: Georgia Tech



- **Georgia Tech was the first World-wide CoC and first Americas CoC**
- **David A. Bader Executive Director of High-Performance Computing and a Professor in Computational Science and Engineering, a division within the College of Computing, at Georgia Institute of Technology.**
- **Dr. Bader also serves as Director of the Sony-Toshiba-IBM Center of Competence for the Cell Broadband Engine Processor located at Georgia Tech.**
- **David received his Ph.D. in 1996 from The University of Maryland.**
- **He is an NSF CAREER Award recipient, an investigator on several NSF and NIH awards, was a distinguished speaker in the IEEE Computer Society Distinguished Visitors Program, and a member of the IBM PERCS team for the DARPA High Productivity Computing Systems program.**